

Docket No.: L&L-I0050



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By: h p.

Date: September 6, 2001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Jens Sauerbrey et al.
Applic. No. : 09/932,891
Filed : August 20, 2001
Title : Variable Clock Configuration for Switched OP-AMP Circuits

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

U. S. Patent 5,994,960 (Baschirotto et al.) dated November 30, 1999;

European Patent Application 0 689 286 A1 (Castello et al.) dated December 27, 1995;

A. Baschirotto et al.: "A 1V CMOS fully differential switched-opamp bandpass $\Sigma \Delta$ modulator", Solid State Circ. Conf. 1997 (ESSCIRC 97), pages 152-155

M. Steyaert et al.: "Switched-Opamp, a Technique for Realising full CMOS Switched-Capacitor Filters at Very Low Voltages", Solid State Circ. Conf. 1993 (ESSCIRC 93), pages 178 - 181.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the Applicants.

Respectfully submitted,

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For Applicants

Date: September 6, 2001

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